Sea	arch No	tes

Application/Control No.	Applicant(s)/Patent under Reexamination	
09/945,558	JONES ET AL.	
Examiner	Art Unit	
Kamran Emdadi	2667	

SEARCHED			
Class	Subclass	Date	Examiner
370	398	4/26/2005	KE
370	400	4/26/2005	KE
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INTERFERENCE SEARCHED			
Class	Subclass	Date	Examiner
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SEARCH NOT (INCLUDING SEARCH)
	DATE	EXMR
A processorless central switch that uses logic gates to handle switching.	4/26/2005	KE
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